**Ryan Laur**

**Benjamin Wheeler  
2/21/2022**

**Report – Lab 1**

[Figure 1 - FSMD 1-Process Modelsim, showing no errors 2](#_Toc96365582)

[Figure 2 - FSMD 2-Process Modelsim, showing no errors 2](#_Toc96365583)

[Figure 3 - FSM + D Modelsim, showing no errors 3](#_Toc96365584)

[Figure 4 - FSM 1-Process Quartus, showing no errors 3](#_Toc96365585)

[Figure 5 - FSM 2-Process Quartus, showing no errors 3](#_Toc96365586)

[Figure 6 - FSM + D Quartus, showing no errors 4](#_Toc96365587)

[Figure 7 - Datapath Schematic 5](#_Toc96365588)

Figure 1 - FSMD 1-Process Modelsim, showing no errors

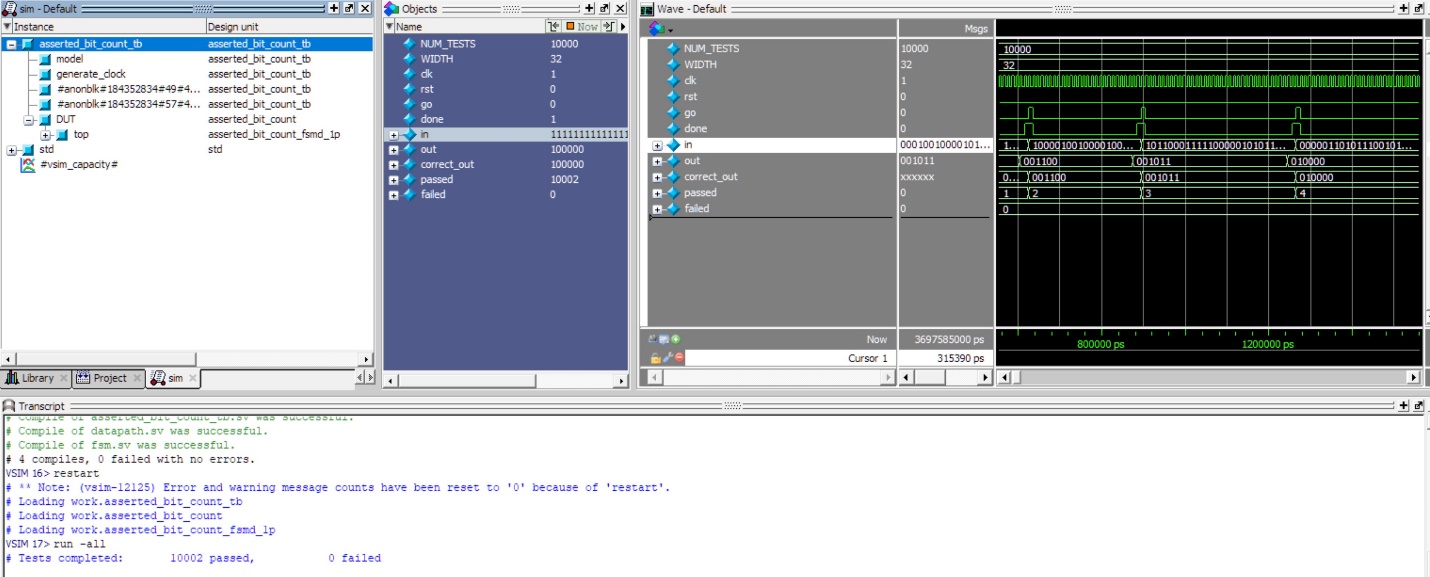
****

Figure 2 - FSMD 2-Process Modelsim, showing no errors

Graphical user interface

Description automatically generated

Figure 3 - FSM + D Modelsim, showing no errors

Graphical user interface

Description automatically generated

Figure 4 - FSM 1-Process Quartus, showing no errors

Calendar

Description automatically generated

Figure 5 - FSM 2-Process Quartus, showing no errors

Text, letter

Description automatically generated

Figure 6 - FSM + D Quartus, showing no errors

Text

Description automatically generated

Figure 7 - Datapath Schematic

Diagram

Description automatically generated